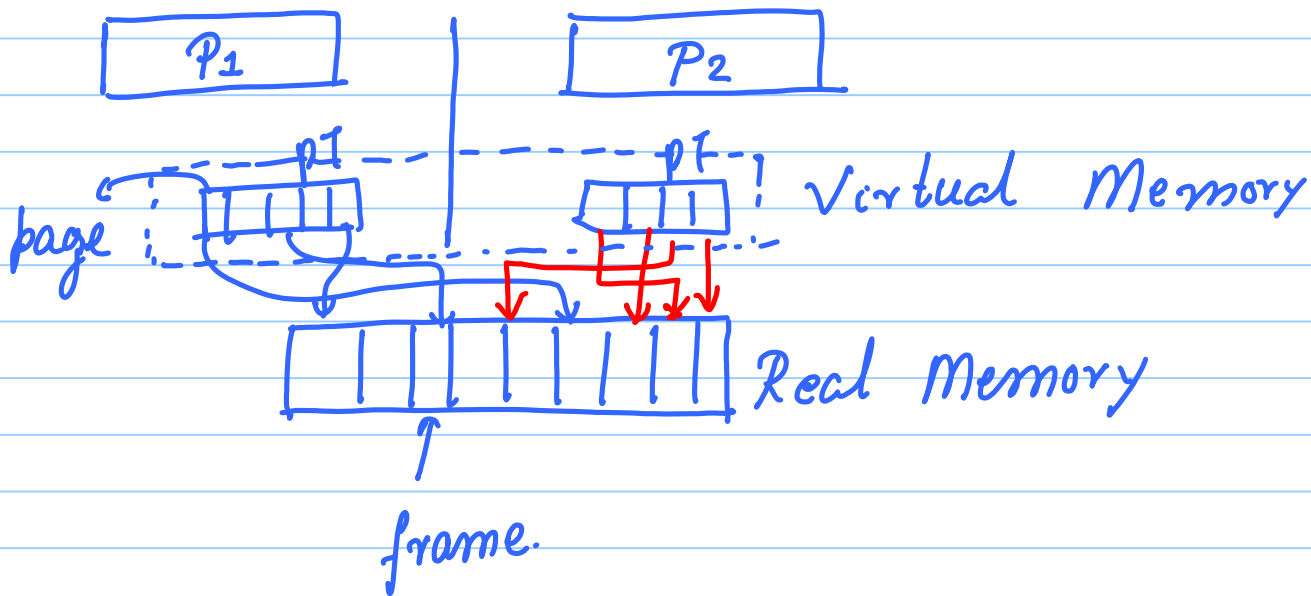
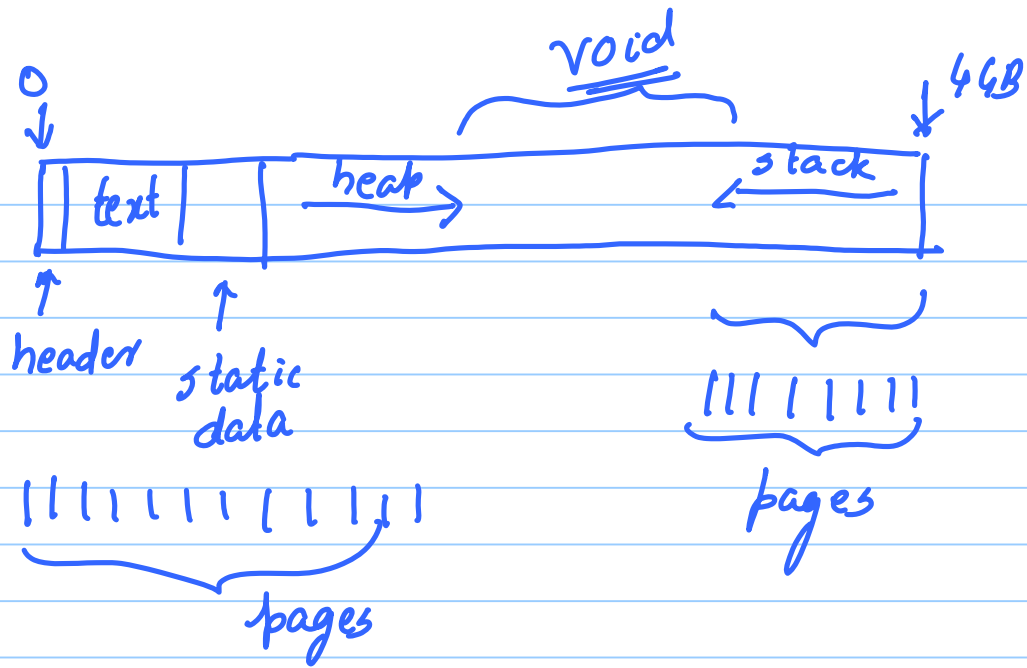


Oct 12th

Virtual Memory.

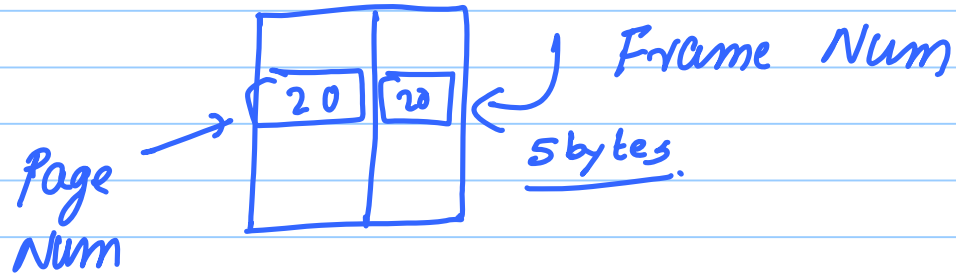


Memory Layout

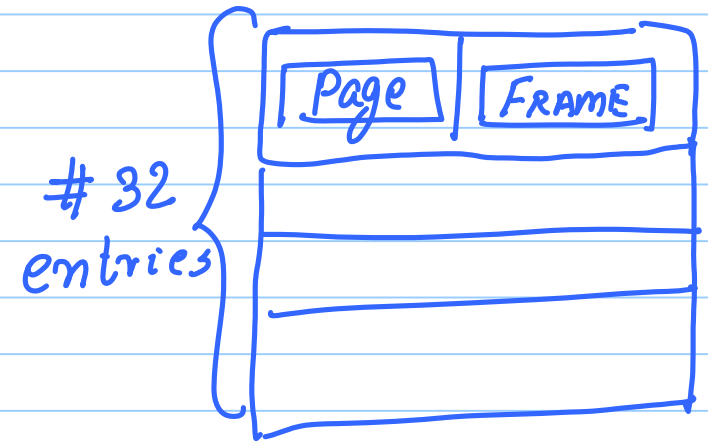


Page Table/
Mapping
Table

Page Table



TLB (Translation Lookaside Buffer)

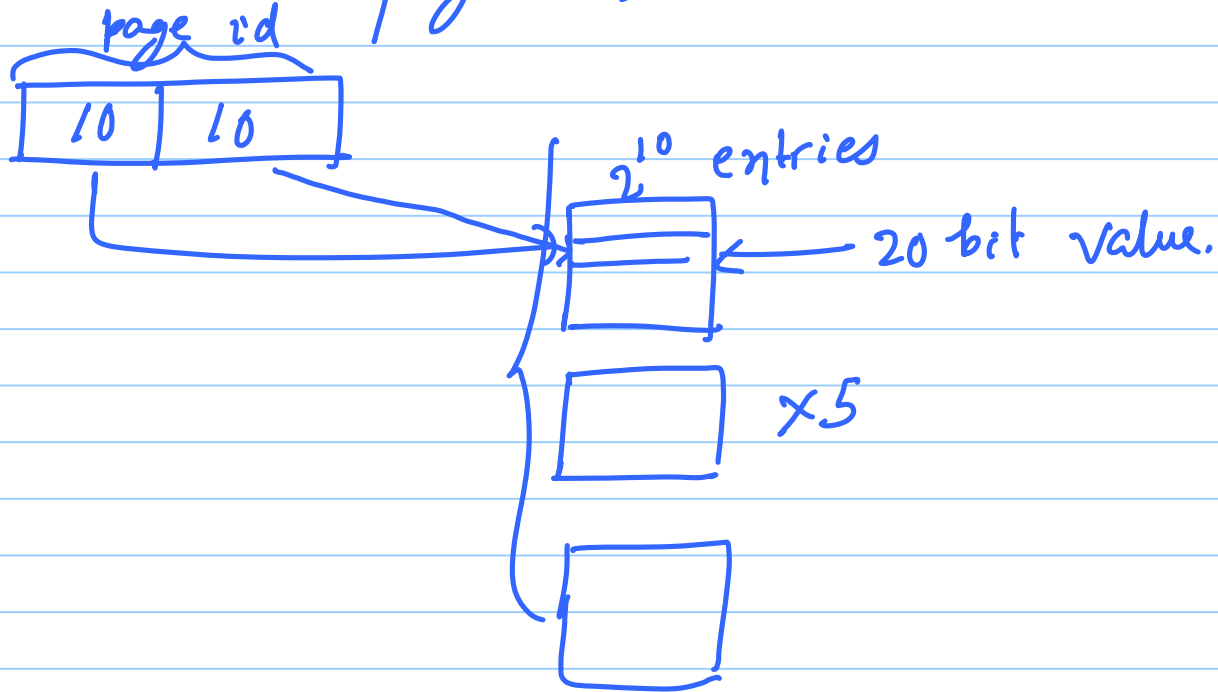


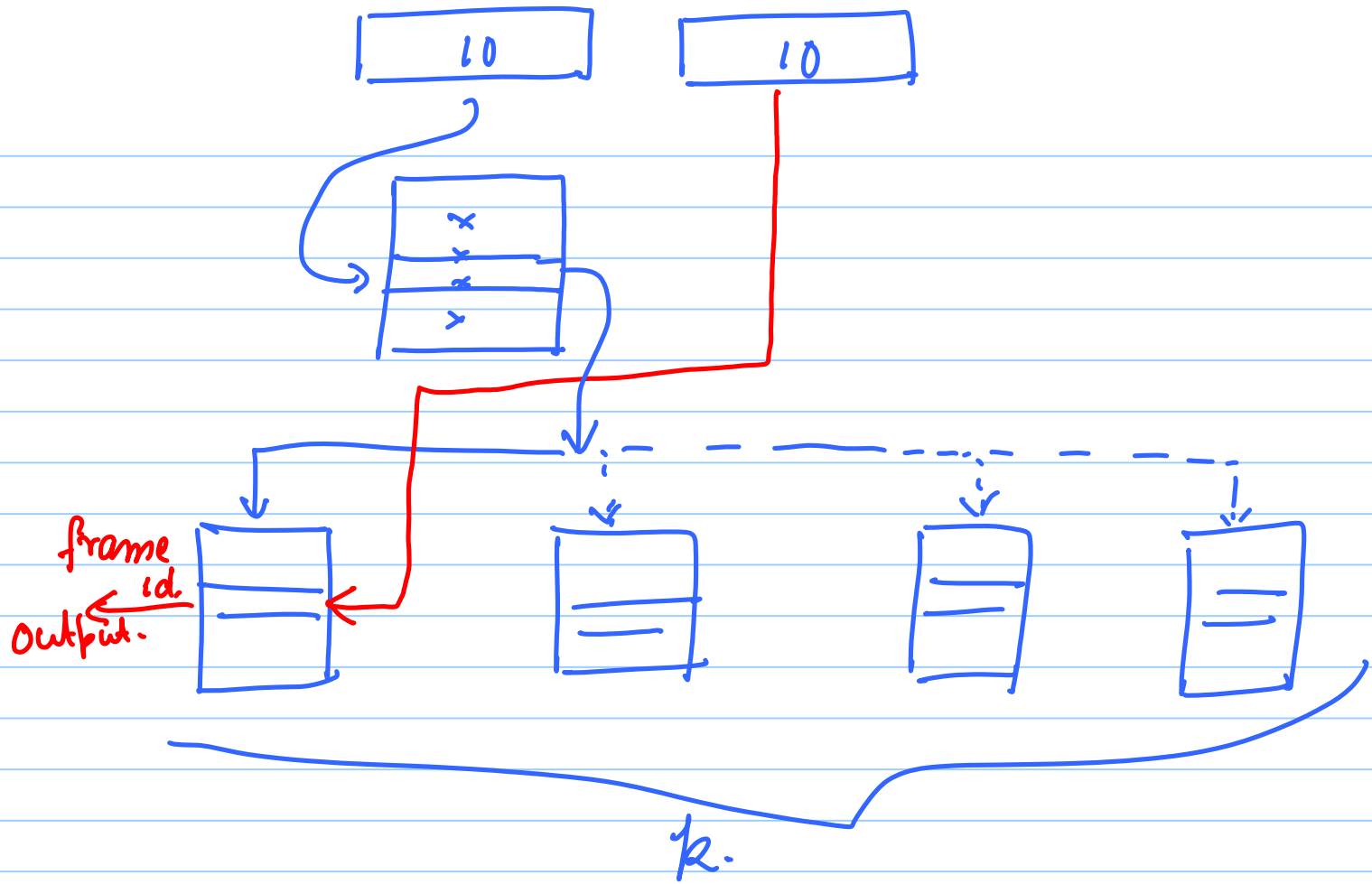
TLB Hit → mapping found in

TLB
TLB Miss → not found.

More Optimal Page Table.

Multi-level page table.





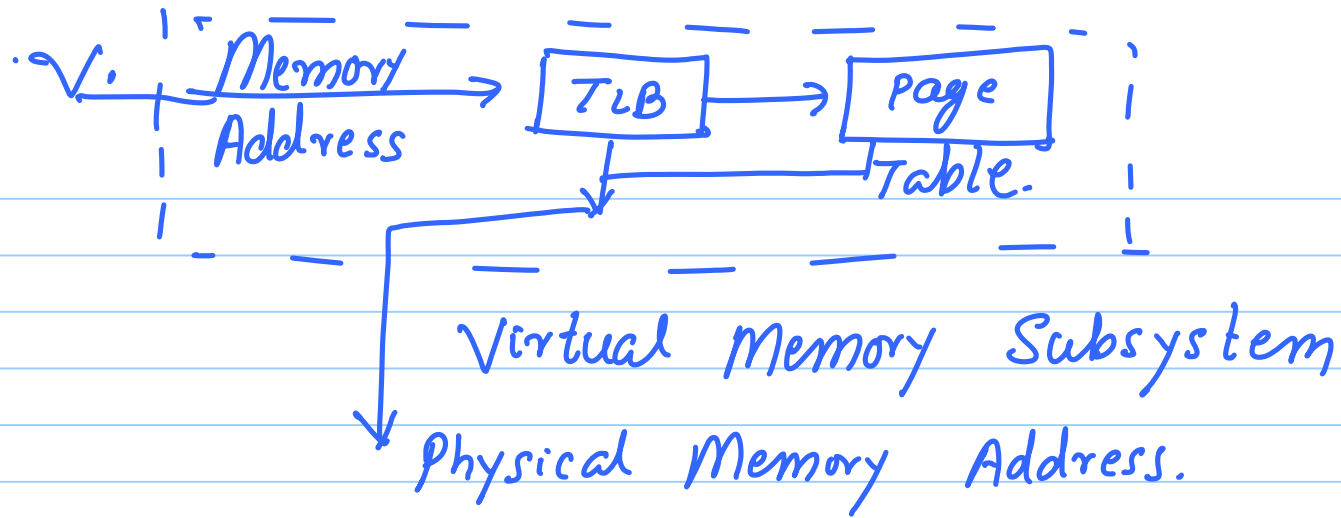
Allocate a new entry.

1) Maintain a list of empty frames.

2) Upon a Page Table Miss

(a) Pick any empty frame
and create a mapping.

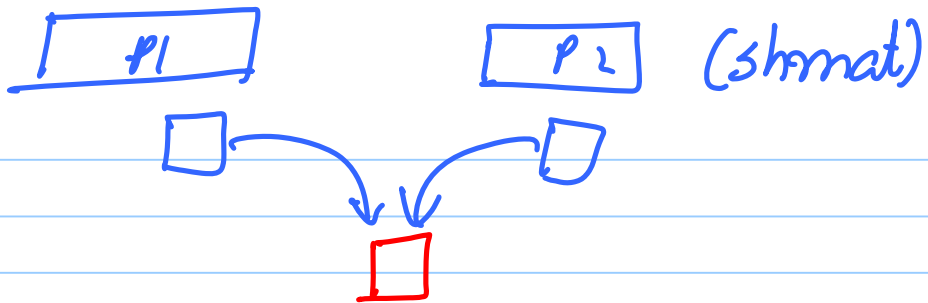
(b) Insert into the page Table.



In general: strict isolation between programs

IPC (Inter Process Communication)

Request OS to create a
common mapping



Swap space.

